



# SMARC











MICROPROCESSOR ARCHITECTURE

## i.MX 8M Plus





Based on the SMARC format, this **SOM + CARRIER Solution** is designed to offer high performance for applications with high graphics requirements



### APPLICATIONS


 Ho.Re.Ca	 Transportation	 Aerospace	 Industrial
 Naval	 Medical	 Robotics	 Digital Signage
 Automotive	 Household appliances		

### MAIN FEATURES


	<b>CPU</b> NXP i.MX8M Plus (Quad ARM Cortex A53 1.6GHz – ARM cortex M7 800 MHz)
	<b>MASS STORAGE</b> Starting from 8 GB eMMC / micro-SD slot
	<b>RAM</b> LPDDR4 2-4GB
	<b>NETWORKING</b> 1x GB Ethernet + optional PCIe WiFi / Bluetooth 5.3

 **OPERATING SYSTEM**  
Linux Yocto


 **GRAPHICS**  
GC7000UL, 3D and 2D Accelerator

 **VIDEO**  
LVDS interface with 1920x1200 resolution (Full HD) or optional HDMI up to 3840x2160 (4K)

 **USB**  
3 USB 2.0 + 1 USB 3.0 + 1 OTG

 **INTERFACES**  
1xI2C, 1xRS232, 1xRS485, 1xCAN, Mini Pci Express, 1xMIPI CSI 4-lane camera

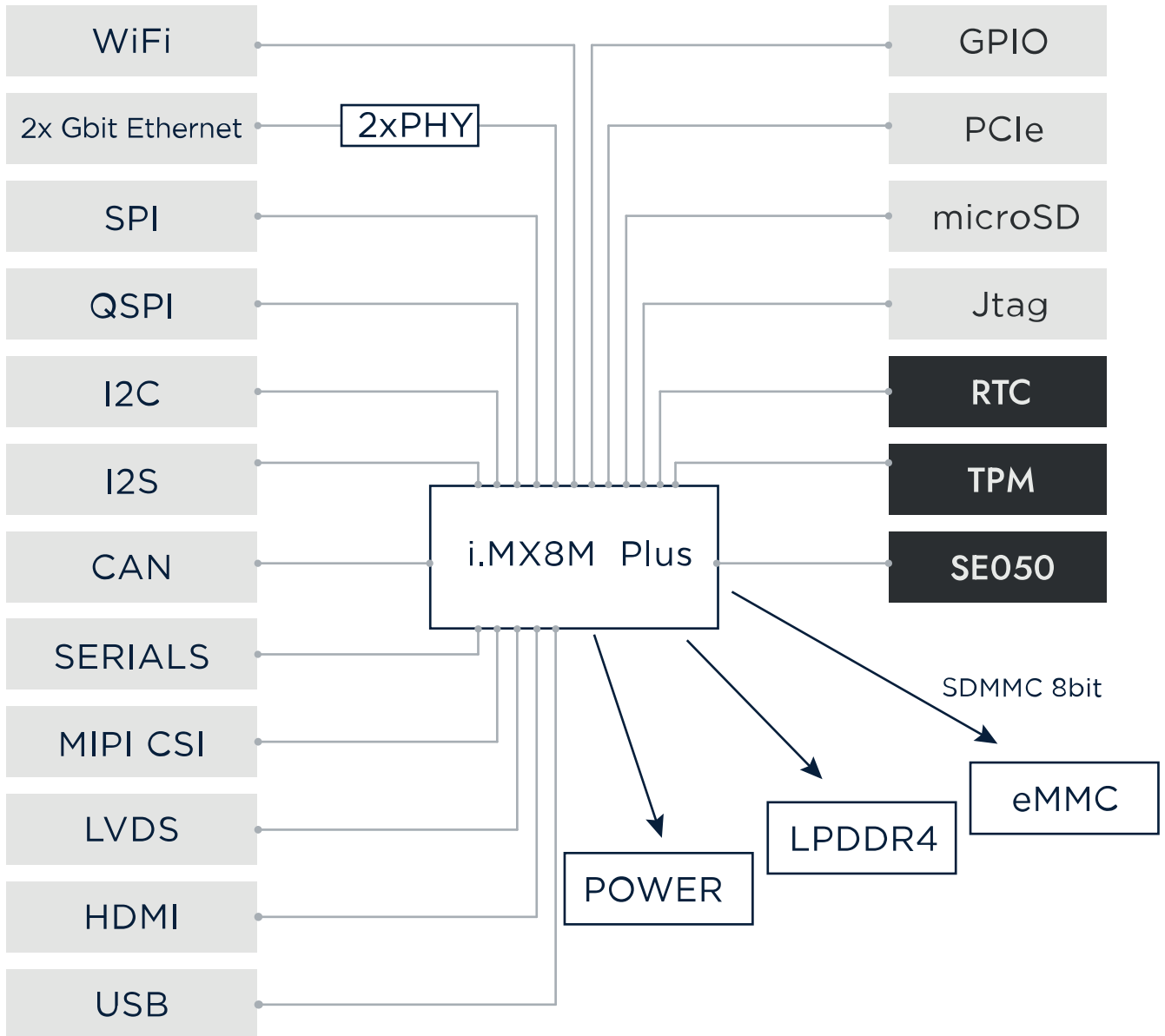
 **I/O**  
5xGPIO + 1xPWM

 **POWER SUPPLY**  
From 15Vdc to 35Vdc / Available voltages on board: 3,3Vdc, 5Vdc, 12Vdc



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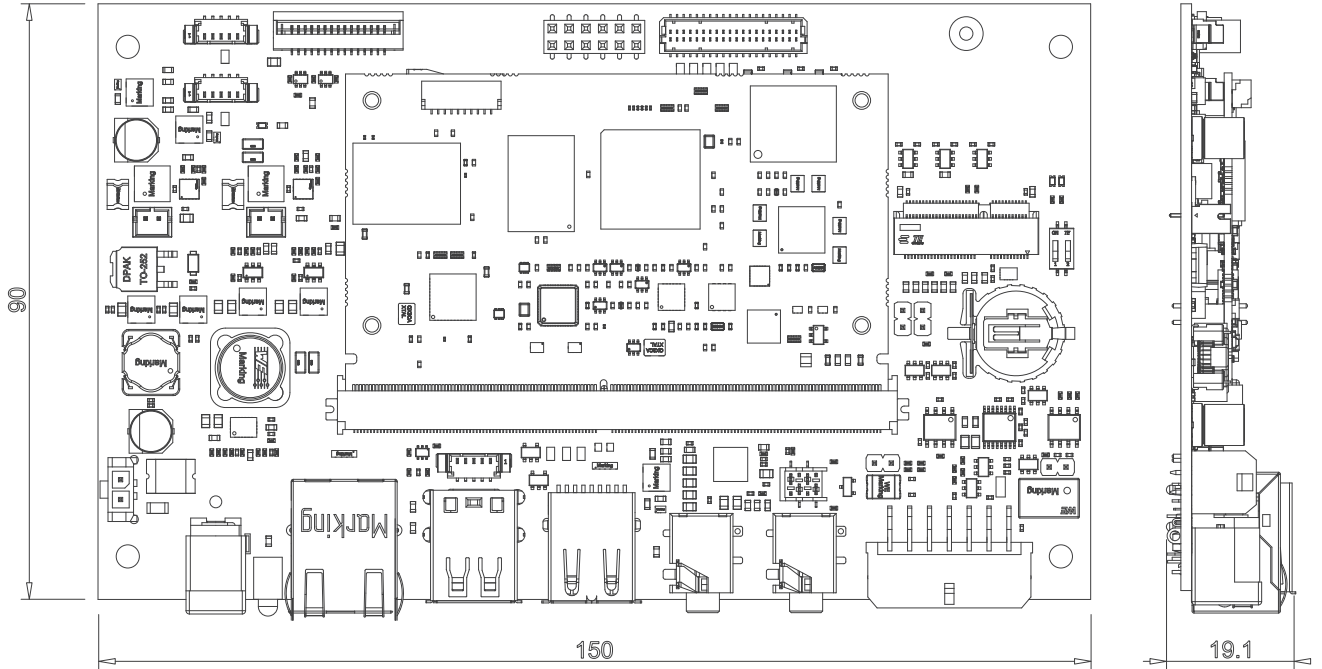
Block Diagram Arm Cortex **i.MX 8M Plus**





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Dimensions



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Interfaces and ports

